

Development and Formal Verification of a Flight Stack for a High-Altitude Micro Glider

Emanuel Regnath Toulouse, 10.10.2017



Approach Devel

Results

Motivation





https://www.brightwork.com/blog/project-failures-boeings-787-dreamliner



Intro Approach

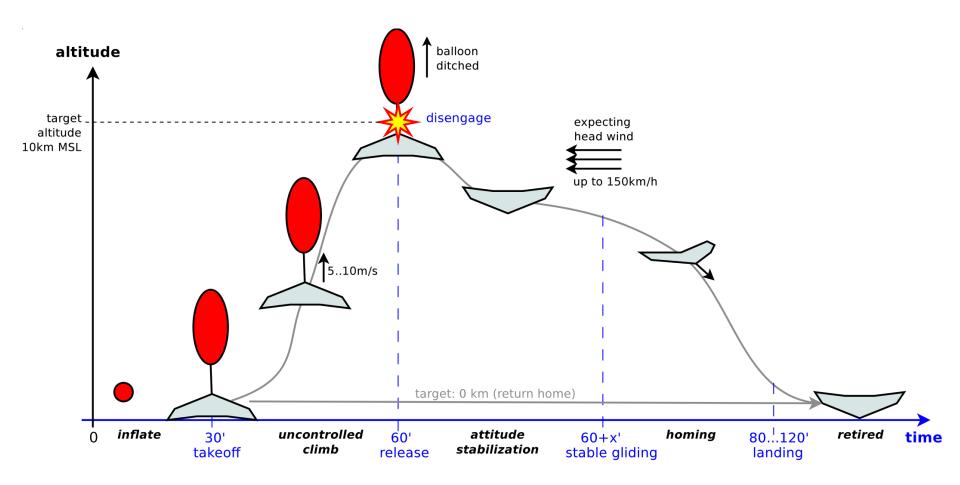
Devel Results



Rejected because considered to ...

- require a lot of additional specification
- require user interaction; little automation
- require experts; results are difficult to understand

Mission – Novel Weather Balloon

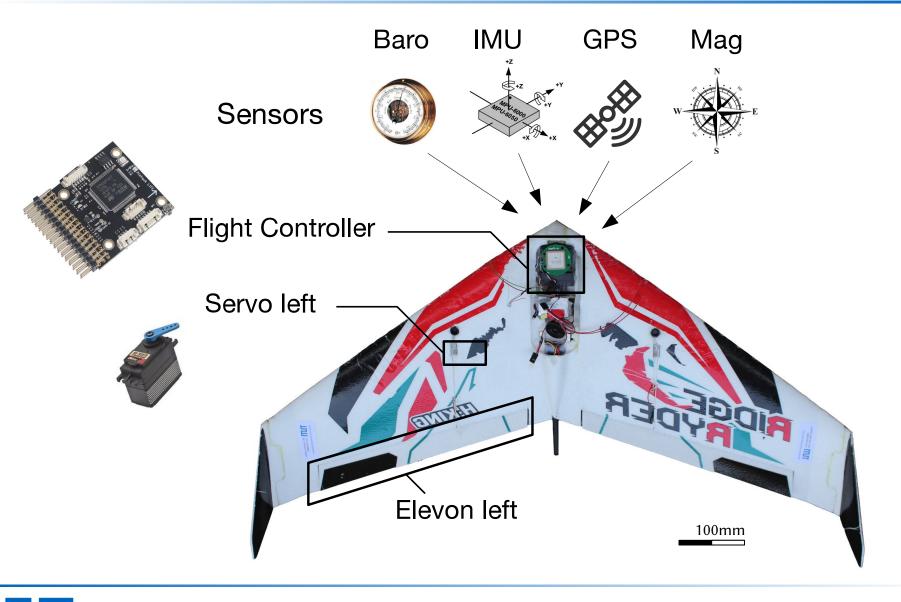


Intro Approach

Devel Results

Introduction to the Scenario



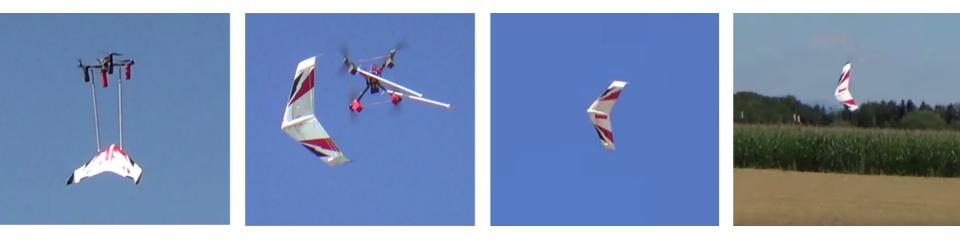


Intro Approach

Devel Results

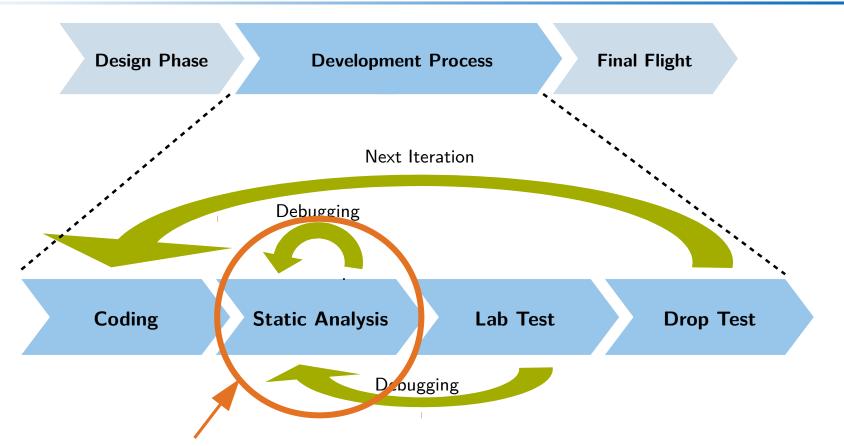
The Roll of System Testing

- Full system tests, including external effects (wind, etc..)
- Risky and high effort (Time&Money) ⇒ as little as possible
- Germany: Must not fly above 100m AGL ⇒ limited



Development Process





- 1. Fast: before compiling
- 2. Normal: Continuous Integration with git

Intro

3. Nightly "deep" verification runs with long timeouts

Devel

Finding Defects – Expectation



Static Analysis

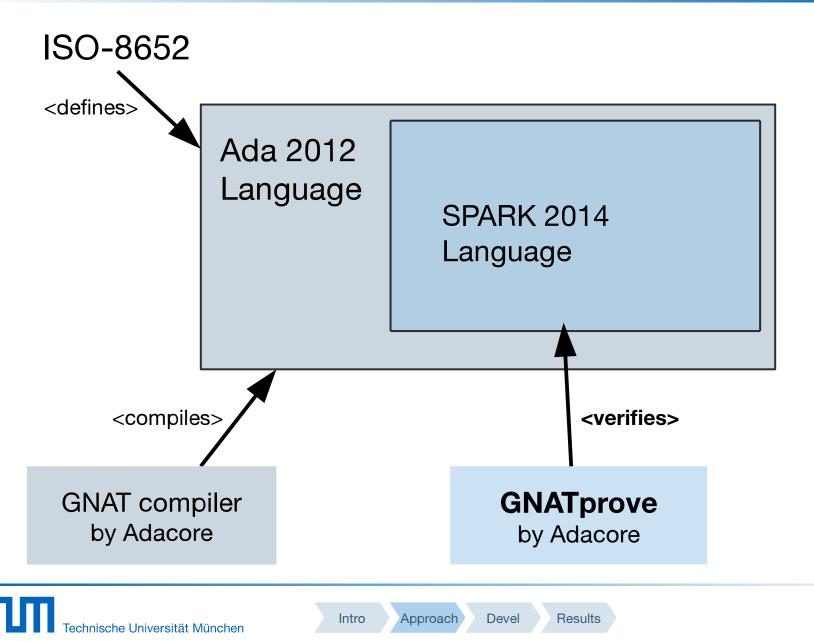
System Testing

Operation

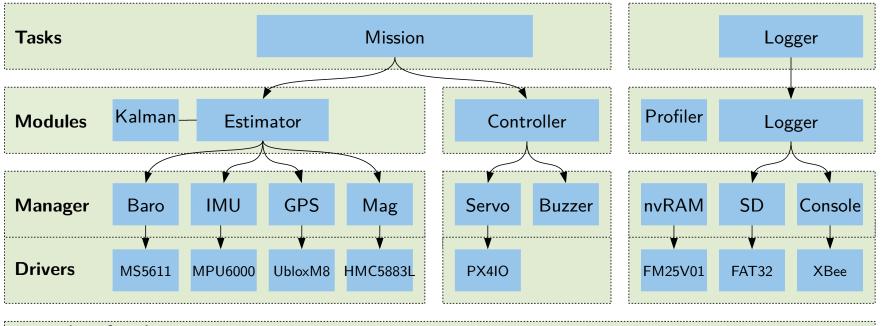
- most by static analysis (each developer & nightly runs)
 - replace unit testing
 - identify under-specification
- few by system testing
 - defects which were missed by static analysis
 - defects which require context beyond source code
 - logging of exceptions: no reproduction issues
- none during operation
 - nevertheless: logging of exceptions & in-air reset

Ada & SPARK









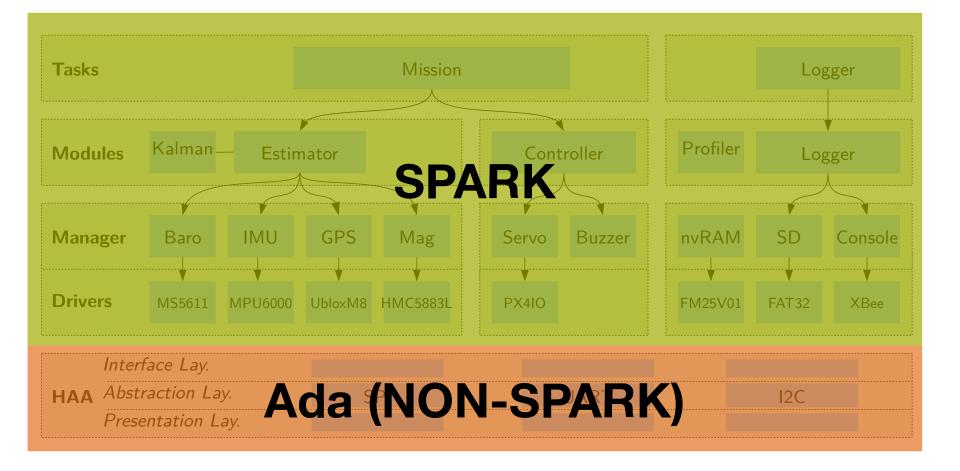
Interface Lay.				
HAA Abstraction Lay.	SPI	UART	I2C	
Presentation Lay.				

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Intro Approach

Devel Results





Approach

Intro

Devel Results



We want to formally verify

Absence of run-time errors Division by zero, overflows

Integration Correctness Valid inputs and outputs Functional Behavior Input to output relation

Information Flow

Global variables, Input to output dependencies

Physical Dimensions

Compliance with physical laws

Intro

Devel



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Devel

```
1
   package MyPack with SPARK_Mode is
2
 3
     subtype Percentage is Natural;
 4
5
     Global_Ratio : Percentage;
 6
7
     procedure set_ratio( alt, maxalt : Integer )
     is begin
8
       Global_Ratio := alt * 100 / maxalt;
9
     end set_ratio;
10
11
12
   end MyPack;
```



```
package MyPack with SPARK_Mode is
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 2
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     is begin
8
       Global_Ratio := alt * 100 / maxalt;
9
10
     end set_ratio;
11
12
   end MyPack;
```

Mypack:9:35 medium: overflow check might fail
Mypack:9:41 medium: divide by zero might fail
Mypack:9:41 medium: range check might fail

```
package MyPack with SPARK_Mode is
 1
 2
 3
     subtype Percentage is Natural;
 4
 5
     Global_Ratio : Percentage;
 6
 7
     procedure set_ratio( alt, maxalt : Integer ) with
        Pre => alt,maxalt > 0 and alt < Integer'Last/100</pre>
 8
      is begin
 9
        Global_Ratio := alt * 100 / maxalt;
10
     end set_ratio;
11
12
13
     set_ratio( 42, 62 );
   end MyPack;
14
```

```
package MyPack with SPARK_Mode is
 1
 2
 3
     subtype Percentage is Natural;
 4
 5
     Global_Ratio : Percentage;
 6
 7
     procedure set_ratio( alt, maxalt : Integer ) with
 8
        Pre => alt,maxalt > 0 and alt < Integer'Last/100</pre>
      is begin
 9
        Global_Ratio := alt * 100 / maxalt;
10
     end set_ratio;
11
12
      set_ratio( 42, 62 );
13
   end MyPack;
14
Mypack:10:35 info: overflow check proved
Mypack:10:41 info: division check proved
Mypack:10:41 info: range check proved
Mypack:13:3 info: Precondition proved
```

```
package MyPack with SPARK_Mode is
1
 2
 3
     subtype Tar_Alt is Integer range 10 .. 10_000;
     subtype Alt is Integer range 0 .. 100_000;
4
     subtype Percentage is Natural;
 5
 6
7
     Global_Ratio : Percentage;
8
9
     procedure set_ratio( val : Alt; max : Tar_Alt )
     is begin
10
       Global_Ratio := val * 100 / max;
11
12
     end set_ratio;
13
   end MyPack;
14
```



```
package MyPack with SPARK_Mode is
 1
 2
 3
      subtype Tar_Alt is Integer range 10 .. 10_000;
      subtype Alt is Integer range 0 .. 100_000;
 4
 5
      subtype Percentage is Natural;
 6
 7
     Global_Ratio : Percentage;
 8
 9
      procedure set_ratio( val : Alt; max : Tar_Alt )
      is begin
10
        Global_Ratio := val * 100 / max;
11
12
     end set_ratio;
13
14
   end MyPack;
Mypack:11:35 info: overflow check proved
Mypack:11:41 info: division check proved
Mypack:11:41 info: range check proved
```





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Flow Analysis



```
package MyPack with SPARK_Mode is
 1
 2
 3
     subtype Percentage is Natural;
 4
 5
     Global_Ratio : Percentage;
 6
 7
     procedure set_ratio( alt, maxalt : Integer )
     is begin
8
       Global_Ratio := alt * 100 / maxalt;
9
10
     end set_ratio;
11
12
   end MyPack;
```

Mypack:9:19 info: initialization of "Global_Ratio" proved



Approach Devel Results



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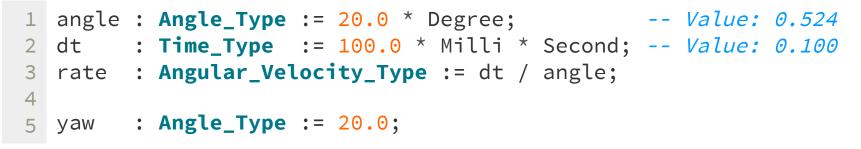
Dimension Checking

Scientific: angular rate = 20 deg / 100 ms = 200 deg/s

C Program:

1 Float angle = 20; 2 Float dt = 0.1; 3 4 Float rate = dt / angle;

SPARK Program:





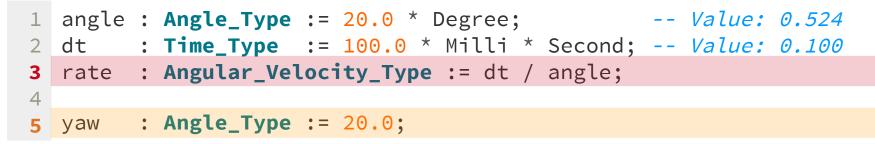
Dimension Checking

Scientific: angular rate = 20 deg / 100 ms = 200 deg/s

C Program:

1 Float angle = 20; 2 Float dt = 0.1; 3 4 Float rate = dt / angle;

SPARK Program:



Mypack:3:17 dimensions mismatch in assignment
Mypack:3:17 expected dimension [A.T**(-1)], found [T.A**(-1)]
Mypack:5:17 warning: assumed to be "20.0 Rad"

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Functional Requirements

```
-- Functional Requirement
 1
   function FR_poshold_iff_no_course() return Boolean is (
 2
 3
       (Have_Course and G_state.mode /= MODE_POSHOLD) or
       (not Have_Course and G_state.mode = MODE_POSHOLD)
 4
 5
   ) with Ghost;
 6
7
   -- Functional Requirement
   function FR_arrive_iff_near_target() return Boolean is (
8
      if (Have_Home_Position and Have_My_Position) then
9
          (dist2home < TARGET_R and G_state.mode = MODE_ARRIVED) or
10
          (dist2home >= TARGET_R and dist2home <= 2.0*TARGET_R)
11
                                                                  or
          (dist2home > 2.0*TARGET_R and G_state.mode /= MODE_ARRIVED)
12
      else G_state.mode /= MODE_ARRIVED
13
14
   ) with Ghost;
15
16
   -- Update the controller mode, depending on state
   procedure Update_Homing() with
17
     Post => FR_poshold_iff_no_course and FR_arrive_iff_near_target;
18
```



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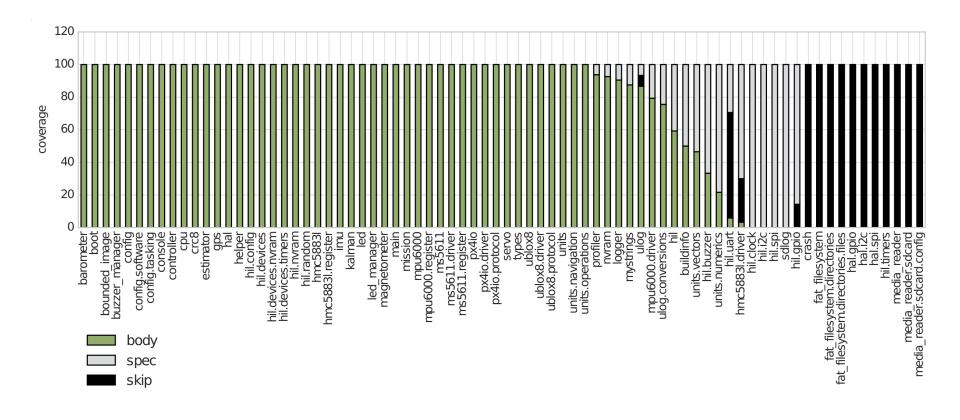
Global variables, Input to output dependencies

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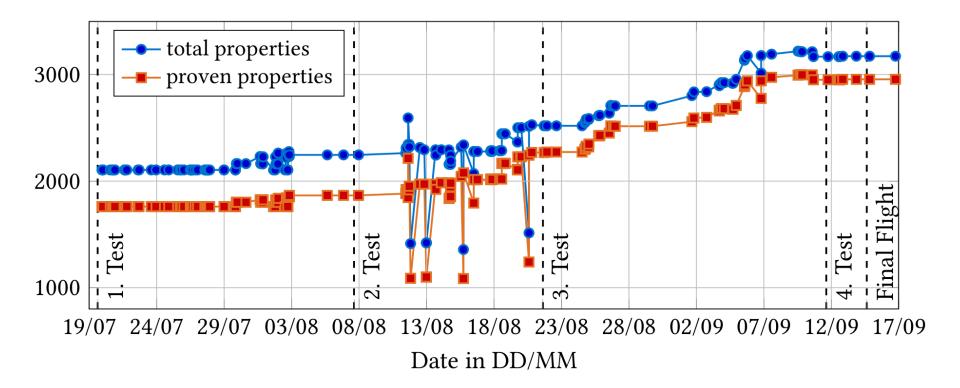
Final GNATprove Results

• SPARK subprogram coverage: 82%









Results

Devel



Final GNATprove Results



Totals of verified properties

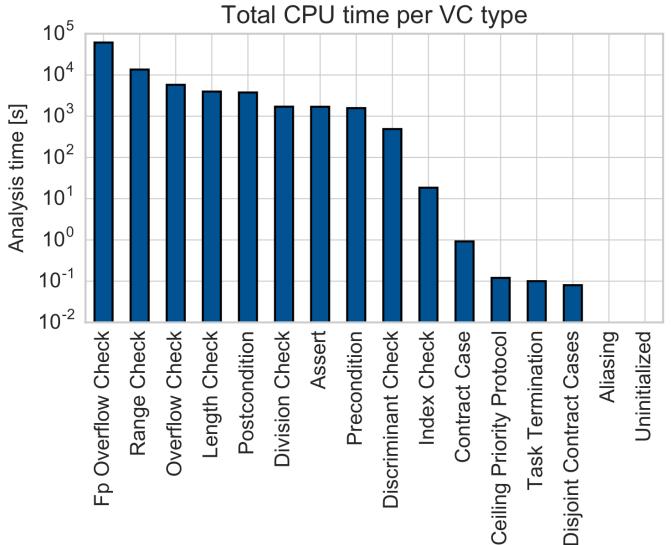
Absence of run-time errors 1487 / 1711 (86.9%) Integration Correctness 277 / 282 (98.2%) Functional Requirements 2 / 2 (100%)

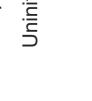
Information Flow 1539 / 1540 (99.9%)

Physical Dimensions ?/? (100%)

Final GNATprove Results







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Final Flight on 2016-09-14

Target Altitude: 6100 m AGL





Approach

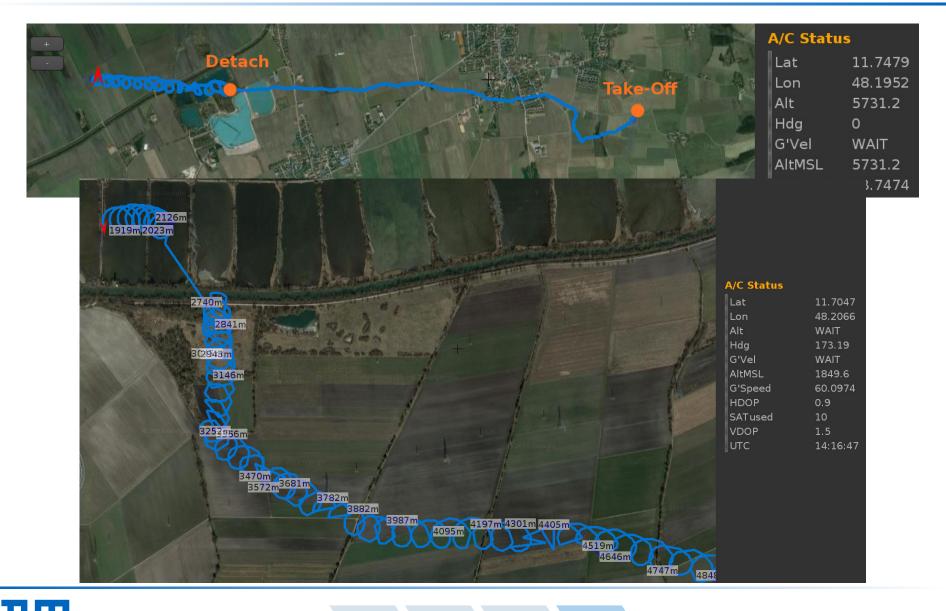
Intro

Devel Results



Final Flight on 2016-09-14





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Approach Devel

Intro

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Finding Defects – Reality



Static Analysis

System Testing

Operation

- most by static analysis (each developer & nightly runs)
 - removed all stupid bugs
 - identified under-specification
- few by system testing
 - masking defects during analysis
 - ignoring failed proofs
 - incomplete specification
- one during operation
 - faulty but non-crashing behavior
 - missed during system testing
 - unverified assumptions about sensor data (beyond code)

Intro

Results

Conclusion



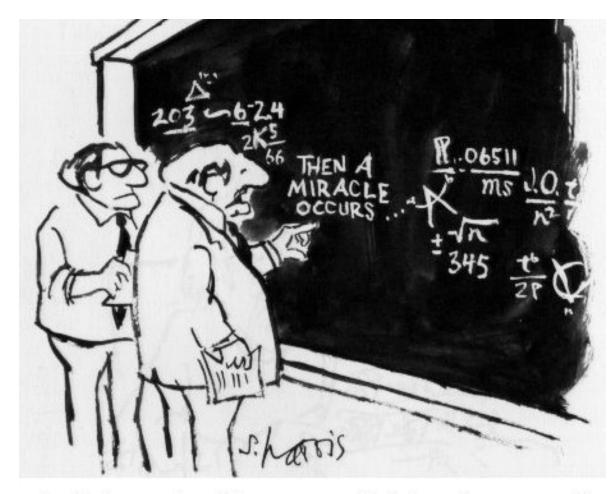
- Very little debugging work
 - Practically no exceptions during system testing
 - No issues with reproduction and isolation of failures
- SPARK tools work very well
 - Defect detection with almost no additional effort
 - Results are precise: Mypack:9:35: overflow check might fail
 - Effective multi-threading: separation of critical tasks
 - Verification automation as continuous integration with git
 - Verification of physical dimensions
 - Floats are difficult but possible
 - Verification of high-level behavior is difficult but possible

code released to open source: https://github.com/tum-ei-rcs/StratoX

Intro

Questions





"I think you should be more explicit here in step two."

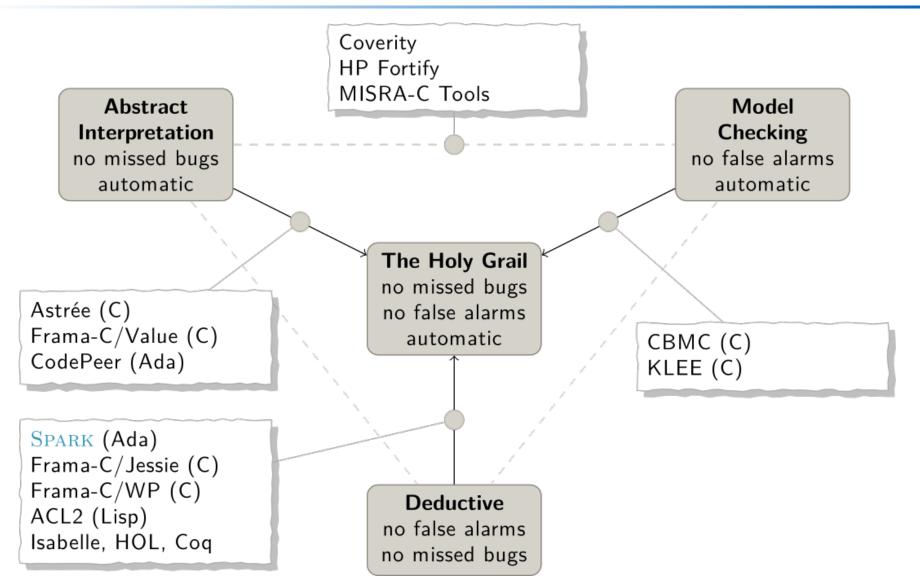
from What's so Funny about Science? by Sidney Harris (1977)

References

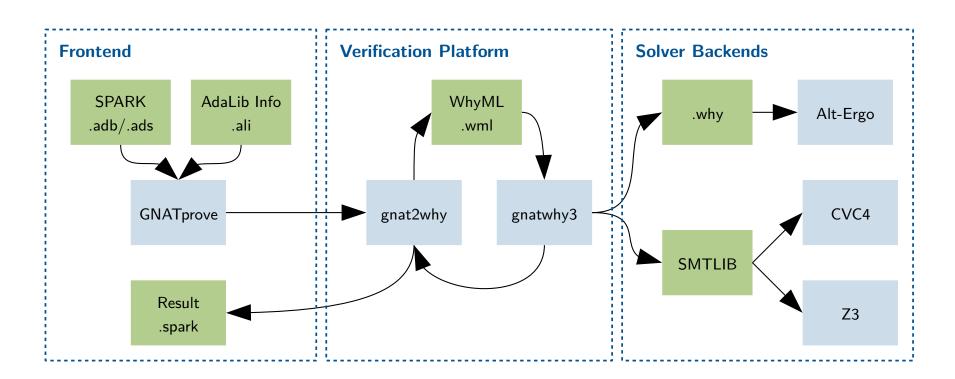


- M. Becker, E. Regnath, Samarjit Chakraborty "Development and Verification of a Flight Stack for a High-Altitude Glider in Ada/SPARK 2014", In 36th International Conference on Computer Safety, Reliability and Security (SAFECOMP), Trento, IT.
- "Airworthiness directives; the boeing company airplanes", Federal Aviation Administration, Tech. Rep. 2015-10066, May 1, 2015.
- Adacore, SPARK 2014 reference manual.
- Adacore, SPARK 2014 user guide, version 18.0.
- Ada reference manual, ISO/IEC Std. 8652, 2012

Formal Verification

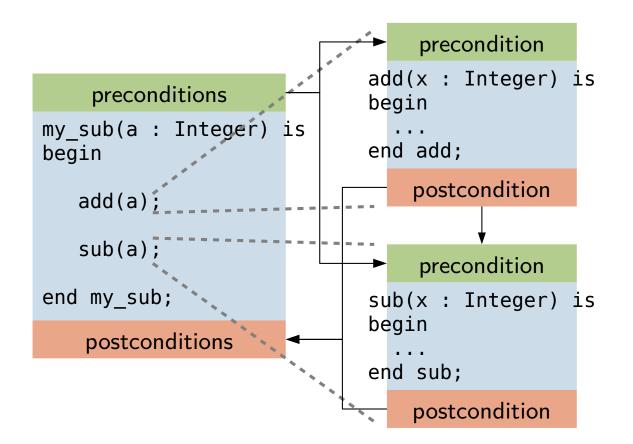


GNATprove internal





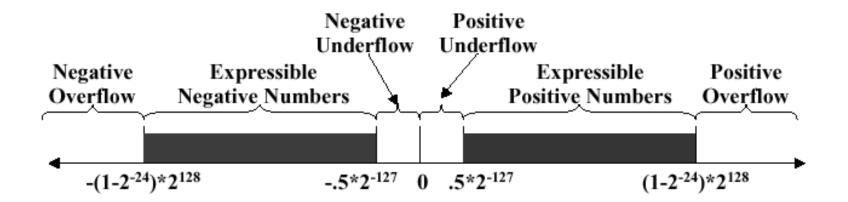
GNATprove internal





FP Underflow





Final GNATprove Results



SPARK Analysis	Total	Flow	Interval	Proved	Justified	Unproven
Data Dependencies						
Flow Dependencies						
Initialization	1540	1510			29	1
Non Aliasing	16	16				
Run-Time Checks	1711		366	1117	4	224
Assertions	15			15		
Functional Contracts	282			277		5
LSP Verification						
Total	3564	1526	366	1409	33	230
Subprogram Coverage :		538 / 1227 (43.8%)			538 / 654 (82%)	
Proven Properties:		3334 /	3564 (93.			
Proven Run-Time E	1487 / 1711 (86.9%)					