

Simulation of Real-Time Scheduling with Various Execution Time Models

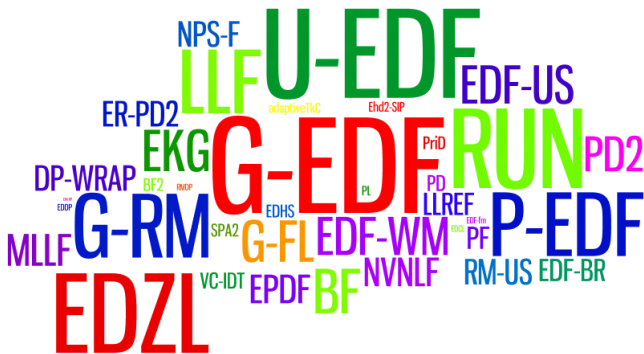
Maxime Chéramy, Pierre-Emmanuel Hladik,
Anne-Marie Déplanche and Sébastien Dubé

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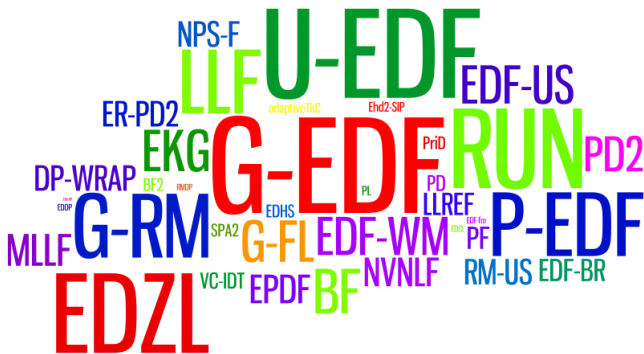
June 19, 2014

9th IEEE International Symposium on Industrial Embedded Systems (SIES)

Many Real-Time Multiprocessor Scheduling Algorithms

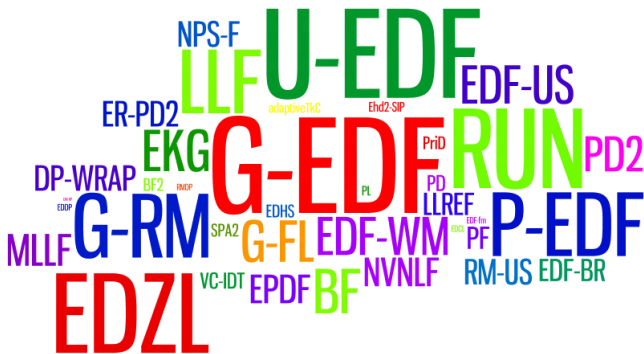


Many Real-Time Multiprocessor Scheduling Algorithms



Requirement: evaluate their behavior and performance.

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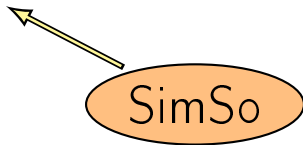
Requirement: evaluate their behavior and performance.

Ojective: propose a friendly and dedicated simulation-based tool.

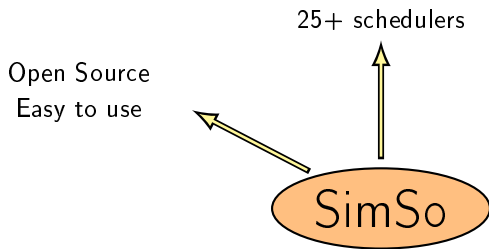


SimSo: a Multiprocessor Real-Time Scheduling Simulator

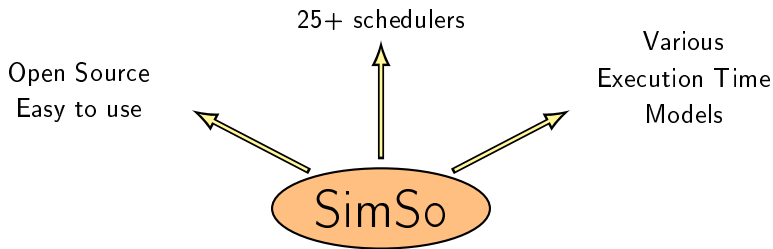
Open Source
Easy to use



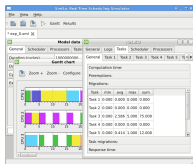
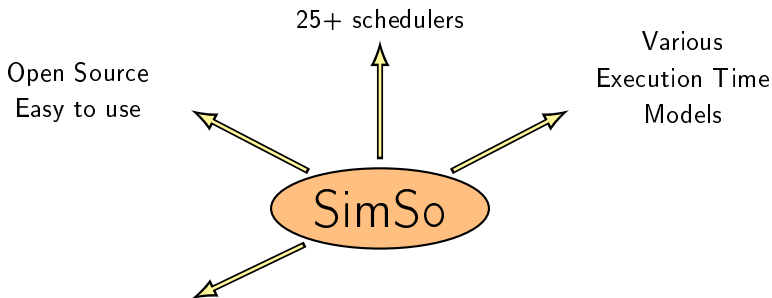
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Graphical User Interface

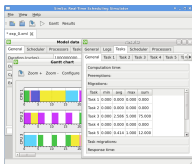
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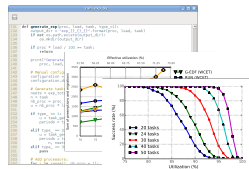
25+ schedulers

Various
Execution Time
Models

SimSo



Graphical User Interface



Script Mode

Execution Time Model 1: Using the WCET vs the ACET

Using the WCET is pessimistic



Upper-bound rarely reached in practice



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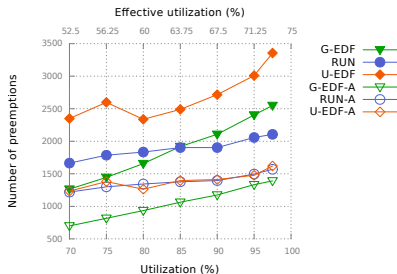
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It makes sense for schedulability analyses.

But we discuss its use for empirical studies focusing on other aspects, including the number of preemptions and migrations.

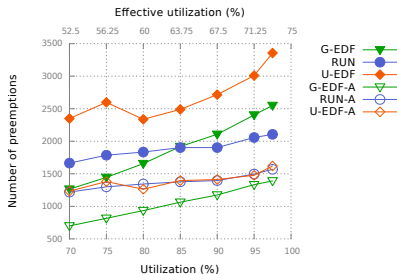
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We ran several simulations using the WCET and a random duration lower than the WCET.



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Using the WCET could introduce a bias in the evaluation

Gives an advantage to schedulers that use the WCET

Some schedulers could benefit from shorter execution times

Some schedulers are robust to punctual overloads

Caches may have a significant impact on the computation time of the jobs, which depends on the scheduling decisions.

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⇒ As a consequence, we want to simulate the cache effects.

Extension of the Liu&Layland task model with:

- SDP: Stack Distance Profile^a
- MIX / API: ratio of memory access per instruction
- CPI: Cycles per Instruction
- Number of executed instructions

^aR.L. Mattson et al. "Evaluation techniques for storage hierarchies". In: *IBM Systems Journal* 9.2 (1970).

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Integration of statistical cache models^a to determine the duration of the jobs dynamically, during the simulation.

^aD. Chandra et al. "Predicting inter-thread cache contention on a chip multi-processor architecture". In: *Proc. of HPCA*. 2005.

Thank you for your attention.

Any question?

→ Come to the poster session,
I'd be pleased to discuss this further
with you.

Simulation of Real-Time Scheduling with Various Execution Time Models

Maxime Chéramy¹, Pierre-Emmanuel Hladik¹, Anne-Marie Déplanche¹ and Sébastien Dubé²

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Abstract

Statis is an extension that aims at facilitating the experimental evaluation of real-time schedulers [1]. One of its features is to present various task-computation time models. Interestingly it looks up to observe that using the WCET as required simulation model introduces a bias in the results. It also enables to take into consideration the impact of the caches on the computation time of the tasks in order to integrate these effects into scheduling analyses.

Motivation

Dozens of real-time schedulers were introduced during the last 30 years, making their evaluation and comparison difficult.

Even if a simulation-based approach may give rise to less accurate results than an execution using a real platform, an idea that simulation offers more convenience and flexibility, and constitutes a good compromise to efficiently evaluate scheduling algorithms in an empirical way.

Statis

- Real-Time Scheduling Simulator
- Multiprocessor system.
- Easy to use and to extend.
- Open Source¹ (and written in Python)
- More than 25 scheduling algorithms



Figure 1: Graphical User Interface of Statis

¹ <http://cherryproject.github.io/statis/>

Execution Time Models

The computation time of the jobs is defined by Statis entities called "Execution Time Models". Statis provides the following ones:

- **WCET**: the computation time matches the given worst-case execution time;
- **ACET**: the computation time is randomly chosen using a normal distribution and bounded by the WCET;
- **Fixed Peak**: the computation time is extended when a preemption occurs;
- **Cache Model**: the computation time is defined by statistical cache models and takes into account the memory access behavior of the tasks.

Discussion about the WCET

Simulating a system so that all tasks meet their worst-case execution time at each job is very problematic since the worst-case is an upper-bound that is rarely reached in practice and it is even more unlikely that it is reached by all the active jobs at the same time.

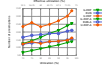


Figure 2: Simulation using WCET and ACET

Using WCET may induce an erroneous evaluation of the scheduling algorithm:

- this could give an advantage to the schedulers that use the WCET as a parameter and highly depend on it;
- some schedulers are designed to take benefits from shorter execution times, which could not be valued;
- some algorithms are naturally robust to a practical overhead and can adapt their response to this demand.

Cache Effects

Cache related preemption delays are often greater than the OS overhead [2].

On a system with shared caches, a job may starve itself another one running on a different processor.

Several schedulers take into consideration the caches (cache-aware schedulers), either by encapsulating (encapsulating cache over-booking, or by using cache space isolation techniques.

Simulating Cache Effects

Statis accepts as input an extended form of the Liu and Layland task model with additional information to characterize the memory behavior of the tasks:

- **Stack Distance Profile**: A distribution of distances that indicates the probability that a cache access is done at a given position in an LRU cache;
 - **MIX or APF**: The number of memory references per instruction;
 - **CPE**: The average number of cycles needed to execute an instruction;
 - **Number of Instructions**: The average number of instructions executed.
- The cache memory organization and characteristics of the hardware architecture being specified, Statis is then able to simulate the cache effects on task execution using statistical models [3]. The accuracy of the results is yet to be confirmed.

Bibliography

- [1] M. Chéramy et al., "Statis: A simulation tool to evaluate real-time multiprocessor scheduling algorithms," in Proc. of RTNDS 2014.
- [2] C. Mural and A. Ben, "The effect of context switches on cache preemptions," *ISQPEAN*, vol. 28, no. 4, 1994.
- [3] M. Chéramy et al., "Simulation of real-time multiprocessor schedulers with caches," in Proc. of 36th International Conference on AUTOTEST-2015.