Covering Step Graph

François Vernadat, Pierre Azéma, François Michel
E-mail: {vernadat,azeama,michel}@laas.fr

LAAS-CNRS
7 avenue du Colonel Roche F-31077 Toulouse cedex - France

Abstract. Within the framework of concurrent systems, several verification approaches require as a preliminary step the complete derivation of the state space. Partial-order methods are efficient for reducing the state explosion due to the representation of parallelism by interleaving. The covering step graphs are introduced as an alternative to labelled transition systems. A transition step consists of several possibly concurrent events. In a covering step graph, steps of independent transitions are substituted as much as possible to the subgraph which would result from the firing of the independent transitions. Attention must be paid to the case of conflict and confusion. An algorithm for the “on the fly” derivation of step graphs is proposed. This algorithm is then extended to behaviour analysis by means of observational equivalence. A performance evaluation is made with respect to other methods.

Keywords: concurrent systems, state space exploration, partial-order, verification methods

1 Introduction

The state space derivation represents the preliminary step of several verification methods for concurrent systems. This approach is made attractive by the existence of efficient and automatic verification techniques, such as bisimulation and model-checking. The combinational explosion is the main limitation of this approach.

Many studies are currently in progress for reducing this problem: on the fly bisimulation [FM 90], symbolic marking graph derivation through symmetrical folding [Jen 87], so-called partial order techniques which attempt to avoid the combinational explosion resulting from the concurrency interpretation by means of interleaving: persistent sets [Val 89], sleep sets [GW 91]. An other direction, followed in [Esp 93, McMil 95], avoids the state explosion by using model checker directly on the description of the system (unfolding of Petri Nets).

The partial order techniques (see [WG 93] for a general survey) represent the framework of the approach developed in this paper. The basic principle of these approaches consists into considering a single specific path among all the sequences which possess the same Mazurkiewicz’s trace [Maz 87]. In the case of persistent or sleep sets, only a subset of enable transitions is examined, the derived graph is then a subgraph of the whole graph.
The proposed approach visit all the transitions, but some independent events are put together to build a single transition step, the firing of this transition step is then atomic. The resulting graph is referred as covering step graph (CSG). A notion of step graph appears in [Rei 85]: this graph is obtained in a standard way except in presence of independently enable transitions where a step gathering these transitions is considered as an additional event and put to the set of transitions. The standard LTS Consequently is a sub-graph of the Reisig’s step graph. The covering step graph proposed here, is radically different because steps of independent transitions are substituted as much as possible to the subgraph which would result from the firing of the independent transitions. The potential benefit of such a substitution may be exponential with respect to the number of “merged” independent transitions. Some examples, in the paper, show that this potential benefit may effectively be obtained.

The covering step graph supplies an alternative structure to the traditional reachable state graph. The CSG analysis allows the control of global reachability properties, such as liveness or presence of deadlock. Furthermore, step graphs may be used for checking behaviour properties such as observational equivalence. An algorithm for the ”on the fly” derivation of CSG is proposed. This algorithm is then extended to behaviour analysis by means of observational equivalence. A performance evaluation is made with respect to other methods.

Section 2 presents the covering step graph definition and the general properties preserved by such structures. Section 3 shows how to modify a standard enumeration algorithm in order to obtain a step graph. This algorithm is adapted in section 4 for computing a step graph observationally equivalent to the regular state graph. A preliminary assessment of the proposed approach is developed in section 5, by comparing it with stubborn and symmetry methods in the case of a standard database example. Current work and extensions are finally mentioned.

2 Covering Step Graphs

2.1 Basic Notions

Labeled Transition Systems (LTS) (definition)
A LTS is a quadruple < S, s₀, T, → > where : S is a set of states, s₀ a distinguished state in S, T is a set of transition labels, → is a set of transitions (→ ⊆ S × T × S).
Notations:
s→² t s′ iff ∃s′ ∈ S : (s, t, s′) ∈ →, s→¹ t s′ iff not s→² t.s
s→¹ s′ to mean that (s, t, s′) ∈ →
s₀ w u sₙ iff w = t₁t₂ . . . tₙ and s₀ → t₁ s₁ → t₂ s₂ . . . , sₙ → tₙ sₙ

2.1.1 Independence Relation
Let Σ =< S, s₀, T, → > be a LTS and t a binary relation over the set of transitions (t ⊆ T × T)
The complement relation of $\hat{l}$ is called conflict, or dependency, relation. The conflict relation is denoted $\hat{\circ}$.

**Independency relation in Petri Net** [Rei 85]

Let $R = \langle P, T, Pre, Post \rangle$ a (Place/Transition or Condition/Event) net, $m_0$ an initial marking and $G(R, m_0)$ the reachability graph associated with the marked net $\langle R, m_0 \rangle$.

For each transition $t \in T$ we define: $\bullet t := \{ p \in P : Pre(p, t) \neq \emptyset \}$

$\star^* := \{ p \in P : Post(p, t) \neq \emptyset \}$ and $\star^* := \star^* \cup \star^*$.

**Place/Transition Nets** Let $\langle P/T \rangle \subseteq \{ T \times T \}$ defined as $t_1 \parallel_{P/T} t_2$ iff $\bullet t_1 \cap \bullet t_2 = \emptyset$.

For each Place/Transition marked net $\langle R, m_0 \rangle$,

$\langle P/T \rangle$ is an independency relation over $G(R, m_0)$.

**Condition/Event Nets** Let $\langle C/E \rangle \subseteq \{ T \times T \}$ defined as $t_1 \parallel_{C/E} t_2$ iff $\star^* t_1 \cap \star^* t_2 = \emptyset$.

For each Condition/Event marked net $\langle R, m_0 \rangle$,

$\langle C/E \rangle$ is an independency relation over $G(R, m_0)$.

### 2.1.2 Mazurekiewicz’s Traces [Maz 87]

**Concurrent Alphabet:** A concurrent alphabet is a couple $\mathcal{E} = (\alpha, \#)$ where $\alpha$ is an alphabet and $\#$ is a reflexive and symmetric relation. The complementary relation $\#^C$ of relations $\#$ in $\alpha$ is called independency relation in $\alpha$

**Mazurekiewicz’s Traces:**

Let $\mathcal{E}$ be concurrent alphabet $(\alpha, \#)$, the equivalence relation in $\alpha^*$, $\equiv_\mathcal{E}$ is defined by: $W \equiv_\mathcal{E} W'$ iff there exists a finite sequence $(W_0, W_1, \ldots, W_n)$ such that $W_0 = W$ and $W_n = W'$ and

$W_i \equiv 1 \geq i \geq n : \exists u, v \in \alpha^*, \exists (a, b) \in \#^C$ such that $W_{i-1} = ubav$ et $W_i = uabv$

Relation $\equiv_\mathcal{E}$ defines the trace equivalence over $\mathcal{E}$. Equivalence classes of $\equiv_\mathcal{E}$ are called traces over $\mathcal{E}$. A trace generated by a string $w$ is denoted by $[w]_\mathcal{E}$.

**Trace properties:**

* $\equiv_\mathcal{E}$ is the least congruence in the monoid $(\alpha^*, \varepsilon)$ satisfying:

$(a, b) \in \#^C \Rightarrow a \cdot b \equiv_\mathcal{E} b \cdot a$

* Let $\Sigma, \hat{l}, \mathcal{E}$ be respectively a LTS $\langle S, s_0, T, \rightarrow \rangle$, an independency relation over $\Sigma$ and an associated concurrent alphabet $(T, \hat{l}^C)$, then:

$\forall s \in S, w_1, w_2 \in T^*$ such that $s \overset{w_1}{\rightarrow} s_1$ et $s \overset{w_2}{\rightarrow} s_2 : [w_1]_\mathcal{E} = [w_2]_\mathcal{E} \Rightarrow s_1 = s_2$

### 2.2 Transition Steps

In what follows, $\Sigma$ denotes a LTS and $\hat{l}$ an independency relation over $\Sigma$.

---

1 For a binary relation $R \subseteq U \times U$, $R^C$ denotes the binary relation $(U \times U) \setminus R$
2.2.1 Transition Steps (definition)
The transition set $E$ defines a transition step wrt $l$ iff $t_1, t_2 \in E : t_1 \vdash t_2$
In the sequel, $\text{Step}(T, l)$ denotes the set of all transition steps derived from $T$
wrt $l$. When no confusion is possible, we note $\text{Step}(T)$ instead of $\text{Step}(T, l)$.
Property: $E \in \text{Step}(T, l), F \subset E \Rightarrow F \in \text{Step}(T, l)$

2.2.2 Traces and transition steps
Set of sequences associated with a transition step
Let $\text{Seq}$ be the mapping $\mathcal{P}(T) \mapsto \mathcal{P}(T^*)$ defined by:
$\text{Seq}(\emptyset) = \{\epsilon\}$ and $\text{Seq}(E) = \bigcup_{e \in E} \text{Seq}(E \setminus \{e\}) \cup e$
where for $\Omega \in \mathcal{P}(T^*)$ and $w \in T^* : \Omega \otimes w = \{\omega, w : \omega \in \Omega\}$
Property: $\forall E \in \text{Step}(T, l), \omega_1$ and $\omega_2 \in \text{Seq}(E) \Rightarrow [\omega_1]_{(T, l)c} = [\omega_2]_{(T, l)c}$
Trace of a transition step The former property allows to define the transition step trace as the trace of a string associated with the step by means of function $\text{Seq}$. For $E \in \text{Step}(T, l)$, $[E]_{(T, l)c} = \text{def} \ [E]_{(T, l)c}$ where $E \in \text{Seq}(E)$
Trace of a sequence of transition steps is defined as the trace of the string obtained by concatenation of the strings associated with each step of the sequence: For $E_1, E_2, \ldots, E_n \in \text{Step}(T, l)^*$,
$[E_1, E_2, \ldots, E_n]_{(T, l)c} = \text{def} \ [E_1, E_2, \ldots, E_n]_{(T, l)c}$ where $E_i \in \text{Seq}(E_i)$

2.2.3 Extension of the reachability relation to transition steps
Let $\Rightarrow$, be the extension of $\rightarrow$ to transition steps defined by:
$\forall s, s' \in S, \forall E \in \text{Step}(T) : s \xrightarrow{E} s'$ iff $\forall e \in E : s \xrightarrow{e}$ and $\exists w \in \text{Seq}(E) : s \xrightarrow{w} s'$

2.2.4 Extensions of $l$ to string and transition steps
Support of a string: Let $\| \mid \|$, be the mapping $T^* \mapsto \mathcal{P}(T)$ defined by:
$\|\epsilon\| = \emptyset$ and $\|w, \omega\| = \{w\} \cup \|\omega\|$
Extensions of $l$
Let $l \subset \text{Step}(T) \times \text{Step}(T)$ defined by $T_1 \vdash T_2$ iff $T_1 \times T_2 \subset \{l\}$
Let $l \subset \text{Step}(T) \times T^*$ defined by $T_1 \vdash T_2$ iff $T_1 \times \|w\| \subset \{l\}$
Let $l \subset T^* \times T^*$ defined by $w_1 \vdash w_2$ iff $\|w_1\| \times \|w_2\| \subset \{l\}$

2.2.5 Diamond properties
1. For $t \in T, w \in T^*$: If $s \xrightarrow{t}, s \xrightarrow{w}$ and $t \vdash w$ then $s \xrightarrow{w} s'$ and $s \xrightarrow{\hat{w}} s'$
2. For $w_1, w_2 \in T^*$: If $s \xrightarrow{w_1}, s \xrightarrow{w_2}$ and $w_1 \vdash w_2$ then $s \xrightarrow{w_1} s'$ and $s \xrightarrow{w_2} s'$
3. For $w \in T^*, E \in \text{Step}(T)$: If $s \xrightarrow{w}, s \xrightarrow{E}$ and $E \vdash w$ then $s \xrightarrow{w} s'$ and $s \xrightarrow{\hat{w}} s'$
   $\forall E \in \text{Seq}(E)$
4. For $E_1, E_2 \in \text{Step}(T)$: If $s \xrightarrow{E_1}, s \xrightarrow{E_2}$ and $E_1 \vdash E_2$ then $s \xrightarrow{E_1 \cup E_2}$
   And $s \xrightarrow{\hat{E_1}} s'$ and $s \xrightarrow{\hat{E_2}} s'$ $\forall E_i \in \text{Seq}(E_i)$
Sketches of proofs: (1) by recurrence on |w|, (2) by recurrence on |w_i| and property (1), (3) is a consequence of (2) \( (E \upharpoonright w \Rightarrow E \upharpoonright w) \). (4) \( E_1 \upharpoonright E_2 \Rightarrow E_1 \cup E_2 \in \text{Step}(T) \) and property 3.

2.3 Covering Step Graph

This section introduces the Covering Step Graph definition. The general reachability properties preserved by Step Graph are presented.

2.3.1 Covering Step Graph (definition) Let \( \Sigma = \langle S, s_0, T, \rightarrow \rangle \) be a LTS \( \downarrow \) an independency relation over \( \Sigma \) (we let \( \# = \{ \} \))

\( E' = \langle S', s_0', T', \rightarrow' \rangle \) is a Covering Step Graph for \( \Sigma \), wrt \( \downarrow \) iff

1. \( S' \subseteq S \)
2. \( T \subseteq \text{Step}(T \downarrow) \)
3. \( \forall s, s' \in S \cap S' : s \rightarrow s' \ \Rightarrow \ s' \in S' \)
4. \( \forall s \in S \cap S' \, \forall s' \in S' : s \Rightarrow s' \)

\[ s \Rightarrow s' \] implies \begin{cases} \exists s'' \in S', \exists \omega \in T^* \exists P \omega' \in T^* : s' \Rightarrow s'', s \Rightarrow \omega \Rightarrow s'' \text{ and } [w.w'](T, \#) = [P \omega \omega'](T, \#) \\ n \end{cases}

Note: any LTS may be viewed as a CSG by considering \( \downarrow = \{ \} \)

Milner's Scheduler This scheduler is described in [Mil 85], \( n \) sites cyclically execute action \( a_i \) then action \( b_i \). A scheduler constraints the execution of the whole system in such a way that the \( n \) sites alternatively perform actions \( a_1, a_2, \ldots, a_n \). Figure 1 depicts the net of the whole system (scheduler + \( n \) sites), then the corresponding LTS, and a derived CSG.

![Fig. 1. Milner's Scheduler](image-url)

Condition (1), each state of the step graph is a state of the standard LTS. For the Milner’s scheduler, \( t_1 \upharpoonright t_2 \) iff \( t_1 \neq t_2 \), consequently each subset of \( T \) constitutes a step of transitions and (2) holds, Condition (3) means that each transition step in the CSG (S1) corresponds to a firing sequence in the standard LTS (S): for instance \( \{A_2, B_1\} \) 6 corresponds to \( {A_2^2, B_1^1} \) (or \( {B_1^1, A_2^2} \)).

Finally, condition (4) expresses a “covering condition” between the firing sequences of the standard LTS and the step sequences of the CSG.
In $S$, $1^{a_1 \rightarrow b_1, a_1}$, condition 4 of definition 2.3.1 ensures that a "covering" step sequence exists in $S1$. The initial sequence, $a_1, a_2, b_2, b_1$, is extended by $a_1$, in the CSG the following step sequence $\Omega = \{a_1\}, \{a_2, b_1\}, \{a_1, b_2\}$ covers it: $1 \rightarrow 2$ and $1^{a_1, a_2, b_2, b_1, a_1} \rightarrow 2$ with $[a_1, a_2, b_2, b_1, a_1] = \{\{a_1\}, \{a_2, b_1\}, \{a_1, b_2\}\}$

Section 3.2 presents examples satisfying conditions 1), 2) and 3) and violating condition 4).

### 2.3.2 Reachability Preservation

This section shows how general reachability properties such as liveness or deadlock may be checked directly on CSG.

**Liveness**

$L$ = $< S, s_0, T, \rightarrow >$, is E-Live for $E \subseteq T$ if $\forall s, \exists s \in S, \forall e \in E, \exists \sigma \in T^*$ such that $s \xrightarrow{\sigma} E = < S', s_0', T', \rightarrow >$, is E-Live if $\forall s, \exists s' \in S, \forall e \in E, \exists \sigma \in T^*$, $\exists u \in T$ such that $s \xrightarrow{\sigma}$ with $e \in u$

**Property** If $E'$ is a CSG for $E$ then $E'$ is E-Live if $E$ is E-Live ($\Leftarrow$) (resp $\Rightarrow$) is a direct consequence of condition 3 (resp 4) of definition 2.3.1

**Deadlock preservation** Let $Sink(E) = \{s \in S : \forall t \in T, s \xrightarrow{t}\}$

**Property:** If $E'$ is a CSG of $E$ then $Sink(E) = Sink(E')$

$Sink(E) \subseteq Sink(E')$ (resp $Sink(E') \subseteq Sink(E)$): By absurd by considering a state $s \in S - Sink(E)$ (resp $s' \in S' - Sink(E)$) and by applying condition 4 (resp 3) of definition 2.3.1, this leads to a contradiction.

### 3 On the Fly Covering Step Graph Derivation

This section introduces a basic algorithm for the covering step graph derivation directly from the description of the system. The general conditions to be fulfilled are first described. These conditions are shown to be sufficient conditions. The corresponding complete algorithm is then presented.

### 3.1 Basic Principles

Table 1 describes the general structure of the basic algorithm. This algorithm is similar to a standard algorithm for computing a reachable marking graph, that is $Enabled(q)$ computes the set of transitions enabled by state $q$, and $fire(q, t)$ computes the state obtained from state $q$ by firing transition $t$.

The changes with respect to a standard algorithm are the following.

- The independence relation $\mathcal{I}$ is supplied.
- The enable transitions are split in two subsets by means of functions $T_U$ and $T_M$:
  - $T_u$, defines transitions to be explored in a standard way.
  - $T_m$ defines transitions whose exploration will be conducted within a step. In the sequel, such transitions will be referred as "mergeable" transitions.
- The set of transition steps $II_{T_m}$ built by means of function $II$, whose domain
is the set of mergeable transitions $T_m$
- The implementation of a firing step of transitions.

### 3.2 Sufficient Conditions

Some sufficient conditions are now defined on the sets $T_u, T_m,$ and $\Pi T_m$. These conditions have to be satisfied in order to ensure that the algorithm of Table 1 produces indeed a CSG. These conditions are parameterised by a transitive conflict relation weaker than the initial one. This transitive relation, denoted $\#$, is such that $\# \subset [\#]$, consequently $[\#]^C \subset \varnothing$.

#### 3.2.1 Sufficient Conditions

Table 2 describes the conditions which lead to a CSG derivation. Conditions $CA_1, CA_2$ deal with elements $T_u$ and $T_m$, respectively. Conditions $CB_1, CB_2$ concern the set of transition steps $\Pi T_m$.

Condition $CA_1$ defines a partition of the enable transition set. Condition $CA_2$ implies that no (initial) conflict may occur between a mergeable transition and a disable transition.
\( \forall q \in S : \)

\[
\begin{align*}
(CA_1) & \quad T_m \cup T_u = Enabled(q) \text{ and } T_m \cap T_u = \emptyset \\
(CA_2) & \quad \text{If } t \in T_m \text{ then } t \neq t' \Rightarrow t' \in Enabled(q) \\
(CB_1) & \quad \forall \pi \in II_{\tau_m} : \pi \in Step(T, \phi_C) \\
(CB_2) & \quad \forall P \in Step(Enabled(q), \phi_C), \exists \pi \in II_{\tau_m} : P \subseteq \pi
\end{align*}
\]

**Table 2.** Sufficient conditions on \( T_u, T_m \) and \( II_{\tau_m} \)

Condition \( CB_1 \) implies that no (direct or indirect) conflict exists between transitions within a step. Condition \( CB_2 \) implies that the considered computation steps subsume any subset of strongly independent transitions. Conditions \( CA_2, CB_1 \), and condition \( CB_2 \) are illustrated in subsections 3.2.2, 3.2.3, respectively.

### 3.2.2 Confusion Case

Petri net \( N \) and its associated LTS \( S \) depicted by Figure 2 represent a confusion case [Rei 85]: Transition \( D \) is in conflict with transitions \( A_1, B_1 \). These latter transitions are independent. From state 1 of the LTS, transitions \( A_1 \) et \( B_2 \) may occur. When \( B_2 \) first occurs, \( D \) may then occur; however, when \( A_1 \) first occurs, \( D \) cannot happen.

![Fig. 2. Confusion Case: Illustration of Conditions \( CA_2 \) et \( CB_1 \)](image)

**Motivation for \( CA_2 \):**

Consider LTS \( S1 \) (Fig 2). From state 1, computation step \( \{A1, B2\} \) is built, transition \( A1 \) is in a (potential) conflict with (still disable) transition \( D \). The atomic computation step \( A1, B2 \) hides some internal states, namely states 2 and
3. In state 3, transitions $A_1$, $B_1$, and $D$ are simultaneously enable. Step \{A1,B2\} would prevent the firing of transition $D$, and a deadlock state would be omitted. In this case, "covering" condition (def 2.3.1.4) is made false.

**Motivation for $CB_1$:** Consider LTS $S_2$ (Fig 2). From state 3, computation step \{A1,B1\} might be derived, whereas A1, B1 are in an un-direct conflict, through D (i.e. \{A1,B1\} $\in$ Step($T,l$) but $A1#D$ and $B1#D$)

The sequence $1 \overset{B_2}{\rightarrow} B_1 \overset{B_2}{\rightarrow} D \overset{5}{\Rightarrow} 5$ in $S$ (cf. Fig 2) is not "covered" in $S_2$, in the sense of definition 2.3.1. Because state 5 is a deadlock state, a sequence from state 1 to state 5 must exist. The single sequence of length 4 in $S_2$ is the following:

$\{A_1\}, \{B_2,A_2\}, \{D\} \overset{1}{\rightarrow} \overset{2}{\Rightarrow} \overset{3}{\Rightarrow} \overset{4}{\Rightarrow} \overset{5}{\Rightarrow} 5$ or $\{\{A_1\}, \{B_2, A_2\}, \{D\}\} \neq \{B_2, B_1, B_2, D\}$.

$S_3$ is a CSG for $S$.

### 3.2.3 Maximal Step Coverage

The net depicted by Figure 3 consists of two independent conflict sets. The associated LTS is represented by LTS $S$, CSG $S_2$ is a correct one with respect to the net, but step graph $S_1$ is an incorrect one, because it does not satisfy condition $CB_2$. With respect to $S$, sequence $0 \overset{e_1}{\rightarrow} e_4 \overset{2}{\Rightarrow} 2$ must be a possible sequence of any correct associated CSG. This is not the case for step graph $S_1$: $0 \overset{e_1}{\rightarrow} e_3 \overset{2}{\Rightarrow} 2$ and $0 \overset{e_1}{\rightarrow} e_3 \overset{2}{\Rightarrow} 2$ but $\{\{e_1, e_3\}\} \neq \{\{e_1, e_4\}\}$ and $\{\{e_2, e_4\}\} \neq \{\{e_1, e_4\}\}$

![Fig. 3. Wrong Coverage: Illustration of condition $CB_2$](image)

### 3.3 Sufficient Conditions of Step Graph Computation

The basic algorithm, depicted by Table 1 leads to the correct CSG computation, as far as the conditions given by Table 2 hold. By using the notations of Table 1, the conditions to be verified are given by Table 3.

#### 3.3.1 Proof of conditions (1), (2), and (3)

(2) and (3) are immediate consequences of conditions $CA_1$, $CB_1$ and of the relation $\forall C \subseteq l$

---

2 Trace equality ensures that the two sequences have the same length
Table 3. Verification Conditions

When $s \xrightarrow{E} s'$, two cases have to be considered:

either $E = \{t\}$ then $s \xrightarrow{t} s'$ and $\{t\} \in \text{Step}(T, l)$ or $E = \{t_1, t_2, \ldots, t_k\}$ then $E \in \text{Step}(\text{Enabled}(s), l)$ (cf $\text{CB}_1$) and $s \xrightarrow{E} s'$ (cf prop 2.2.3)

The enumeration starts with $H = \{s_0\}$ ($s_0 \in S$), (2) et (3) imply that any state derived from a state in $H$ is indeed a $S$ state, condition (1) consequently holds.

### 3.3.2 Proof of condition (4)
The following lemma is required; the proof of this lemma is shown in section 3.3.3.

Factorisation Lemma $\forall s \in S \cap S', \forall w \in T^* : s \xrightarrow{w} s' \Rightarrow (a)$ or (b) where

(a) $\exists \in T_u, s \xrightarrow{t, u} s'$ with $[w] = [t, w_1]$

(b) $\exists F \in II T_u, \exists E \subseteq F : s \xrightarrow{E, w_1} s'$ with $[w] = [E, w_1]$ and $(F \setminus E) \parallel \|w_1\|

Proof of (4) is performed by induction on $|\omega|$ (obvious when $|\omega| = 0$).

The property is assumed to hold for rank $k$, and let sequence $\omega$ be such that $|\omega| = k + 1$. Factorisation lemma is valid and two cases occur:

Case (a) is obvious: $t \in T_u$ implies $s \rightarrow s_1$ and $s_1 \xrightarrow{w'} s'$, the induction hypothesis is then applied on $s_1$.

Case (b): $\exists F \in II T_u, \exists E \subseteq F : s \xrightarrow{E, w_1} s'$ with $[w] = [E, w_1]$ and $(F \setminus E) \parallel \|w_1\|

Let $R = F \setminus E$, then $s \xrightarrow{E, w_1} s'$ with $R \parallel w_1$ and $E \parallel R$

The diamond property (2.2.5.3) implies: for $r \in \text{Seq}(R)$, then $s \xrightarrow{r, w_1} s''$ and $s \xrightarrow{w_1, r} s''$

(cf fig 4.a)

---

![Fig. 4. Case (b)]
The case depicted by Figure 4.b is a consequence of $E \oplus R = F$, where $[w.r] = [F.w]$. From state $s$, $s' \rightarrow s$ where $[w_1 \leq k$ and the induction hypothesis may be used: $\exists w' \in T^*, \exists P_{w_1.w'} \in \text{Step}(T)^* : s \rightarrow s_1 \rightarrow s_2 \rightarrow s_3$ and $[w_1.w'] = [P_{w_1.w'}]$ (cf fig 4.c). Finally, $s \rightarrow s_3$ and $s' \rightarrow s_3$. Because of $[w,r] = [F,w_1]$ and $[w_1.w'] = [P_{w_1.w'}]$ the result, $[w.r.w'] = [F.P_{w_1.w'}]$, holds.

3.3.3 Proof of Factorisation Lemma

For technical reasons, factorisation operator $\pi$ is first introduced. This operator extracts from any transition sequence the maximal computation step, or the maximal step prefix. Operator $\pi$ is defined according to the selected conflict relation.

$\pi$ Definition: Let $\pi$ be the mapping $S \times \text{Step}(T) \times T^* \times T^* \rightarrow \text{Step}(T) \times T^*$ such that

$\pi(s, E, w, c) = (E, w)$

$\pi(s, E, w_1, w_2) = (E, w_1, w_2)$ if $E \in T_{w_2}$

$\pi(s, E, w, t.w') = \left\{ \begin{array}{ll}
\pi(s, E \cup \{t\}, w, t.w') & \text{if } t \in T_{w_1} \text{ and } E \cup \{t\} \in \text{Step}(T, \|w\|)
\
\pi(s, E, w, t.w') & \text{if } t \in T_{w_1} \text{ and } E \in \text{Seq}(E)
\end{array} \right.$

$\pi(s, E, w_1, t.w')$ otherwise

$\pi$ Properties

Let $s \in S, w, w_1 \in T^*$ be such that $s \rightarrow s'$ and $\pi(s, \emptyset, w) = (E, w_1)$. Then (1) and (2)

(1) $\exists E s \rightarrow s_1, s_1 \rightarrow s'$ where $[w] = [E, w_1]$

(2) $\forall t \in \|w_1\| : t \rightarrow t'$

proof of factorisation lemma:

Let $s \in S, w, w_1 \in T^*$ be such that $s \rightarrow s'$ and $\pi(s, \emptyset, w) = (E, w_1)$.

As a result of the $\pi$ construction, three cases occur: $E = \{t\}$ and $t \in T_{w_1}$. The first two cases are similar, and the result directly follows from $\pi$ property (1). The third case $E \notin T_{w_1}$ needs to be developed.

By construction, $E \in \text{Step}(T, \|w\|)$, condition $CB_2$ implies that transition subset $F \in T_{w_1}$ exists, verifying $E \subseteq F$. $\pi$ property (1) implies the trace equality: the relation $F \setminus E \equiv w_1$ is the last to be proved.

By absurd, let $R | F \setminus E$ and assume that there exists transition $t' \in \|w_1\|$ in conflict with $t \in R$. Because of $t \in R \Rightarrow s \rightarrow t$, condition $CA_2$ implies $s \rightarrow t'$ and $\pi$ property (2) then holds: $\exists t' \in E$ and $t' \equiv t'$. Finally, $t, t' \in F$ such that $t \|w\| t'$ and $F \in \text{Step}(T, \|w\|)$, this is a contradiction.
3.4 Step Graph of Crossed Conflicts

This section presents a particular conflict relation and definitions of functions $T_M, T_U$, and $\Pi$ providing an algorithm for the step graph derivation, which is a specific instantiation of the former basic algorithm (cf. Table 1).

3.4.1 Weak Conflict and Strong Independence Let relation $\equiv_#$ be the reflexive and transitive closure of relation $\#$. This relation is called weak conflict relation. Relation $\equiv_#$ is an equivalence relation and $\# \subseteq \equiv_#$. Relation $\equiv_#^C$, i.e. the complement relation, is called strong independence relation and $\equiv_#^C \subseteq \l$

3.4.2 Crossed Conflict Step An easy way to obey to rule $CB_2$ is to consider only maximal computation steps. Since conflict relation is an equivalence relation ($\equiv_#$), the associated conflict sets supply a partition of the transition set. The set of maximal computation steps results from the orthoprodut [PF 90] of the equivalence classes of relation $\equiv_#$

Orthoprodut : Let $E$ be a set of sets, $E = \{E_1, E_2, \ldots, E_n\} \subseteq E \subseteq \mathcal{P}(\mathcal{U})$

$\Pi_C(E) = \{\{e_1, e_2, \ldots, e_n\} : (e_1, e_2, \ldots, e_n) \in E_1 \times E_2 \times \ldots \times E_n\}$

Crossed Conflict Step: The set $\Pi_C(T_m/ \equiv_#)$ is called Crossed Conflict Step. Properties:
- $E \in \Pi_C(T_m/ \equiv_#) \Rightarrow E \in \text{Step}(T_m, \equiv_#^C)$ a fortiori $E \in \text{Step}(T_m, \l)$
- $E \in \Pi_C(T_m/ \equiv_#), t \notin E \Rightarrow E \cup \{t\} \notin \text{Step}(T_m, \equiv_#^C)$
these properties follow from $\Pi_C$ definition, and from $\equiv_#^C \subseteq \l$

3.4.3 Crossed Conflict Step Construction

The final algorithm is derived from the basic one (cf table 1) by considering the following functions:

$T_M(T, l) = \{t \in T : t' \# t \Rightarrow t' \in T\}$

$T_U(T, l) = T \setminus T_M(T, l)$

$\Pi(T_M(T, l)) = \Pi_C((T, l)/ \equiv_#)$

Property: This algorithm produces a CSG. Conditions $CA_1, CA_2$ et $CB_1$ hold (trivial). Condition $CB_2$ results from properties of crossed conflict steps.

Examples: The former (right) CSG are computed by this algorithm. In the case of Milner’s scheduler, (section 2.3.1), the gain is exponential: for $n$ sites, the standard LTS consists of $n \times 2^n$ states and $(n^2 + n) \times 2^{n-1}$ transitions, the CSG consists of $n + 1$ states and $n + 1$ transitions.

4 Covering Step Graphs and Observational Equivalence

This section shows how to adapt the previous algorithm in order to generate, according to a set of observable events $T_{Ob}$, a covering step graph observationally equivalent ($\equiv$) to the standard LTS (i.e the LTS underlying the generated
step graph and the standard LTS are \( \equiv \). The observational equivalence definition is recalled, an additional condition required for observational equivalence is given and shown to be sufficient. Finally, a specific algorithm for producing observational equivalent step graphs is supplied.

### 4.1 Observational Equivalence - Weak Bisimulation

**Observation:** Observational Equivalence is defined with respect to a set of observable events \( T_{Ob} \). The definition of Observational Equivalence introduces a particular label \( \tau \) representing internal (un-observable) computation and new transition relation \( \stackrel{\tau}{{\rightarrow}} \) is defined as follows:

\[
s \xrightarrow{\tau} s' \text{ iff } \exists \omega \in (T \setminus T_{Ob})^* : s \xrightarrow{\omega} s',
\]

and for all observable label \( a \), \( s \xrightarrow{\tau^*a} s' \text{ iff } s \xrightarrow{a} s_1, s_1 \xrightarrow{a} s_2 \text{ and } s_2 \xrightarrow{\tau} s' \)

In the sequel, \( s \xrightarrow{\tau^*} s' \) stands for \( s \xrightarrow{\tau} s' \)

**Observational equivalence** (over finite LTS) may be defined as the limit of a decreasing sequence of equivalence relations over the states of the LTS \( (\equiv_0 \subseteq \equiv_1 \subseteq \equiv_2) \) [Mil 85]

\[
\equiv_0 = \cap_{n \geq 0} \equiv_n \text{ where } \equiv_0 = S_1 \times S_2
\]

\[
\equiv_n q \iff \forall a \in (T_{Ob} \cup \{\epsilon\}) \quad s \xrightarrow{\tau^*a} s' \Rightarrow \exists q' : q \xrightarrow{a} q' \text{ and } s' \equiv_{n-1} q' \quad (i)
\]

\[
q \xrightarrow{\tau^*a} q' \Rightarrow \exists s' : s \xrightarrow{\tau^*a} s' \text{ and } s' \equiv_{n-1} q' \quad (ii)
\]

**Weak-Bisimulation:** A binary relation \( B \) over \( S_1 \times S_2 \) is a weak bisimulation iff

\[
(s, q) \in B \iff \forall a \in (T_{Ob} \cup \{\epsilon\}) \quad s \xrightarrow{\tau^*a} s' \Rightarrow \exists q' : q \xrightarrow{a} q' \text{ and } (s', q') \in B \quad (i)
\]

\[
q \xrightarrow{\tau^*a} q' \Rightarrow \exists s' : s \xrightarrow{\tau^*a} s' \text{ and } (s', q') \in B \quad (ii)
\]

**Property:** Observational Equivalence is the largest Weak-Bisimulation.

**Extensions to LTS** For \( i \in \{1, 2\} \) : \( \Sigma_i =< S_i, s_{0i}, T_i, \rightarrow_i > \)

\( \Sigma_1 \equiv \Sigma_2 \text{ iff } s_{01} \equiv s_{02} \)

\( \Sigma_1 \text{ and } \Sigma_2 \) are weak-bisimilar iff there exists a weak-bisimulation \( B \) containing \((s_{01}, s_{02})\)

### 4.2 Sufficient conditions for observational equivalence

The basic algorithm (table 1) for step graphs generation and the associated sufficient conditions (table 2) are maintained. A new condition is introduced with respect to the set of observed events. Notations are those of table 2.

**Free Transitions**

Let \( \#(t) \) be the transition subset which are in conflict with \( t \):

\[
\#(t) = def \{ t' \in T : t' \# t \}
\]
and $\text{Free}(T, \#)$ the transition subset without conflict:
$$\text{Free}(T, \#) = \{ t \in T : \#(t) = \{ t \} \}$$

**Additional condition for observational equivalence:** Each “mergeable” transition $t$ has to be included in a step transition $\pi$ such that: all transitions in $\pi$ (possibly except $t$) are conflict free and according whether $t$ is observable or not, then either $t$ is the single observable transition in $\pi$ or any transition in $\pi$ is unobservable.

More precisely, condition $CB_3$ has to be fulfilled: \( \forall t \in T_m, \exists \pi \in \Pi_{T_m} : t \in \pi, \pi \cap T_{obs} = T_{obs} \cap \{ t \} \& \pi \setminus \{ t \} \subset \text{Free}(T, \#) \)

$CB_3$ admits the two following consequences:

$c_1$: Within a step, there exists at most a single observable transition or a conflict transition (a transition not belonging to $\text{Free}(T, \#)$).

$c_2$: For each unobservable (enabled) transition, there exists a step containing it, and this step is composed of only free and unobservable transitions.

---

**Fig. 5. Step Graph not observationally equivalent: example 1**

---

**Example 1:** Figure 5 depicts a net where two “independent conflicts” are initially enabled, $P$, a step graph for which some steps contain two conflict transitions ($e_2$ and $e_4$ are independent but $e_2, e_4 \in \text{Free}(T_m, l)^C$, then $c_1$ is violated) and $S$ a fragment of the complete LTS associated with the net. Observed events are $o_1$ and $o_2$.

State 8 of LTS $S$ is considered. 8 admits the following experiments: $8 \xrightarrow{e_2} 9$ and $8 \xrightarrow{e_4} 9$. State 8 does not belong to the states of the step graph $P$ and no state of $P$ is observationally equivalent to 8. Now, we consider state 1, a common state of $S$ and $P$. In $S$, 1 $\xrightarrow{o_1}$ 8 while in $P$ 1 $\xrightarrow{o_1^*}$ $q$ where $q \in \{1, 4, 5, 6, 9\}$. Since $q \neq 8$ for each $q \in \{1, 4, 5, 6, 9\}$, we conclude that $S \not\equiv P$.

**Example 2:** Figure 6 depicts net $N$, step graph $P$ associated with and fragment $S$ of the whole LTS associated with the net. Observed events are $o_1$, $o_2$ and $o_3$.

State 3 of LTS $S$ is considered. 3 admits the following experiments: $3 \xrightarrow{o_1^*} 9$ and $3 \xrightarrow{o_2^*} 9$. State 3 does not belong to the states of $P$ and no state of $P$ is observationally equivalent to 3. Now, we consider state 1, a common state of $S$
and \( P \). In \( S, 1 \xrightarrow{\tau_3^*} 3 \) while in \( P 1 \xrightarrow{\tau_3 \tau_4^*} q \) where \( q \in \{7, 9\} \). Since \( 3 \not\equiv 7 \) and \( 3 \not\equiv 9 \), we conclude that \( S \not\equiv P \).

\( c_2 \) (and \( CB_3 \)) is violated in state 1 of \( P \): transition \( e_1 \) is enabled in 1 but no step containing \( e_1 \) is composed of only free transitions\(^3\).

### 4.3 Observational Equivalence between \( \Sigma \) and \( \Sigma' \)

This section shows that the basic algorithm, depicted by table 1, furnishes an observational equivalent step graph, as far as the conditions of table 2 (sufficient conditions for a step graph generation) and condition \( CB_3 \) described in 4.2 (specific to observational equivalence) hold.

The proof is organised as follows: The two next sections show basic properties of free transitions with respect to observational equivalence. Section 4.3.3 shows that for each state, common to the standard LTS and its step graph, each “classical” experiment (\( s \xrightarrow{w} q \)) may be reproduced by means of \( \sim \), that is \( s \xrightarrow{w} q' \), where \( q \equiv q' \) and \( \equiv \) is the observational equivalence defined on the standard LTS. This result is used in section 4.3.4 to build a bisimulation relation between the standard LTS and its step graph.

#### 4.3.1 Diamond properties over free transitions

**Property:** For \( s, s', q \in S, w \in T^* : s \xrightarrow{w} q \) and \( t \in Free(T, \#) \)

If \( s \overset{t}{\xrightarrow{\tau}} s' \) then (\( s' \xrightarrow{w} q' \) and \( q \overset{t}{\xrightarrow{w}} q \)) or \( (w = w_1.t.w_2 \) and \( s' \xrightarrow{w_1} q \) )

**proof:** If \( t \xrightarrow{w} t \) then the result is an immediate consequence of diamond property 2.2.5. Now, consider the case where \( t \# w \). Since \( t \in Free(T, \#) \Rightarrow t \in |w| \), \( w \) may be rewritten as \( w_1.t.w_2 \) where \( t.t.w_1 \). The result follows by applying diamond property 2.2.5.

**Corollary:** For \( s, q \in S, a \in T_{\text{obs}} : s \xrightarrow{a^*} q \) and \( t \in Free(T, \#), t \not\in T_{\text{obs}} \)

If \( s \overset{t}{\xrightarrow{\tau}} s' \) Then (\( s' \xrightarrow{a^*} q' \) and \( q \overset{t}{\xrightarrow{a^*}} q \)) or \( (s' \xrightarrow{a^*} q) \)

\(^3\) \( \{e_1 \in \{e_1, e_2\} \) and \( e_1 \in \{e_1, e_2\} \) but \( \{e_2, e_3\} \subset Free(T, \#)^C \)
4.3.2 Free Transitions and Observational Equivalence

Property: If \( s \xrightarrow{t} s', t \in \text{Free}(T, \#) \) and \( t \notin T_{\text{Obs}} \) then \( s \equiv s' \)

proof: direct consequence of the former corollary.

Corollary: If \( s \xrightarrow{\sigma} s' \) with \( \sigma \in \text{Free}(T, \#)^* \) and \( ||\sigma|| \cap T_{\text{Obs}} = \emptyset \) then \( s \equiv s' \)

proof by induction on \( ||\sigma|| \)

4.3.3 Experiment Preservation

Hiding Morphisms

For \( T_{\text{Obs}} \subset T \) and \( \mathcal{T} \subset \mathcal{P}(T) \) verifying : \( \forall E \in \mathcal{T} \) \( \text{Card}(E \cap T_{\text{Obs}}) \leq 1 \), we define:

\[
F_{\text{Obs}} : T^* \rightarrow T_{\text{Obs}}^* \text{ by: } F_{\text{Obs}}(o,w) = \begin{cases} 
  o.F_{\text{Obs}}(w) & \text{if } o \in T_{\text{Obs}} \\
  \epsilon.F_{\text{Obs}}(w) & \text{otherwise}
\end{cases}
\]

and \( \mathcal{I}_{\text{Obs}} : T^* \rightarrow T_{\text{Obs}}^* \) by \( \mathcal{I}_{\text{Obs}}(P_1.P_2) = \begin{cases} 
  o.\mathcal{I}_{\text{Obs}}(P_2) & \text{if } P_1 \cap T_{\text{Obs}} = \emptyset \\
  \epsilon.\mathcal{I}_{\text{Obs}}(P_2) & \text{otherwise}
\end{cases} \)

Experiment preservation in \( \Sigma \)

For \( s \in S \cap S', s \xrightarrow{\mathcal{I}} q \Rightarrow s \xrightarrow{W} q' \) with \( F_{\text{Obs}}(w) = \mathcal{I}_{\text{Obs}}(W) \) and \( q \equiv q' \) in \( \Sigma \)

Proof by induction on \( |w| \). Obvious when \( |w| = 0 \), the property is assumed to hold when \( |w| = k \) and let sequence \( t.w \), state \( s_i \) such that \( s \xrightarrow{t} s_i \)

According whether \( t \in T_u \) or \( t \in T_m \), two cases occur.

- If \( t \in T_u \) then \( s \xrightarrow{t} s_1 \) and induction hypothesis may be applied from \( s_1 \) implying that \( s \xrightarrow{W} q' \) where \( q \equiv q' \) and \( F_{\text{Obs}}(w) = \mathcal{I}_{\text{Obs}}(W) \), Since \( \mathcal{I}_{\text{Obs}}(t.W) = \mathcal{I}_{\text{Obs}}(t).F_{\text{Obs}}(W) \) the result follows.

\[ 7.a \quad 7.b \quad 7.c \]

\[ \text{Fig. 7. } t \in T_m \]

- If \( t \in T_m \) then because of condition \( \text{CB}_3 \):

  \[ \exists \pi \in \Pi_{T_m} : t \in \pi, (\pi \setminus \{t\}) \subset \text{Free}(T, \#) \land F_{\text{Obs}}(t) = \mathcal{I}_{\text{Obs}}(\pi) \]

(Fig 7.a) Let \( s'_1 \) such that \( s \xrightarrow{T} s'_1 \), the following holds: \( s_1 \xrightarrow{\mathcal{I}} s'_1 \) where \( \pi_{\mathcal{I}} \in \text{Seq}(\pi \setminus \{t\}) \) (cf def 2.3.1). Since \( \pi \setminus \{t\} \subset \text{Free}(T, \#) \), corollary 4.3.2 is valid and it follows that \( s_1 \equiv s'_1 \).

(Fig 7.b) We have \( s_1 \xrightarrow{w} q, s_1 \xrightarrow{\pi} s'_1 \) and \( \pi \uparrow w \) then \( s'_1 \xrightarrow{w} q' \) and \( q \equiv q' \equiv q'' \) (cf prop 4.3.1). Moreover, \( q \xrightarrow{\mathcal{I}} q'' \Rightarrow q \equiv q'' \) (corollary 4.3.2)
Finally, as depicted in Fig 7.c, \( s \in S \cap S' \) and \( s' \xrightarrow{w} q'' \) with \( |w| = k \), and the induction hypothesis may be applied from \( s \) implying that \( s' \xrightarrow{q', q''} q'' \) and \( F_{\text{obs}}(w) = F_{\text{obs}}(W) \). Since \( q'' \equiv q \) and \( F_{\text{obs}}(t) = F_{\text{obs}}(\pi) \), the result follows.

**Corollary:** For \( s \in S \cap S' \), \( a \in T_{\text{obs}} \cup \{ \epsilon \} : s \xrightarrow{a, a, a} q \Rightarrow s \xrightarrow{a, a, a} q' \) and \( q \equiv q' \) in \( \Sigma \)

### 4.3.4 \( \Sigma \) and \( \Sigma' \) are Weak-Bisimilar

Let \( B \) be the subset of \( S \times S' \) defined by \( B = \{ (q, q') \in S \times S' : q \equiv q' \} \) property: \( B \) is a bisimulation between \( \Sigma \) and \( \Sigma' \). (proof is a direct consequence of corollary 4.3.3 and CSG definition)

**Corollary:** \( \Sigma \) and \( \Sigma' \) are observationally equivalent

### 4.3.5 Effective Algorithm

In this section a specific conflict relation and specific functions \( T_M, T_U \) and \( \Pi \) are proposed to obtain an algorithm for the derivation of an observational equivalent step graph which is a specific instantiation of the former basic algorithm (cf Table 1).

1. Functions \( T_M \) and \( T_U \), introduced in section 3.4.3, are maintained:

\[
T_M(T, t) = \{ t \in T : t \neq t' \in T \} \quad \text{and} \quad T_U(T, t) = T \setminus T_M(T, t)
\]

2. Conflict equivalence relation \( \# \) is defined by the following partition of \( T_M \):

\[
T_M = \bigcup_{t \in T} F_{\text{free}}(T_M, \#) \quad \text{where} \quad F_{\text{free}}(T_M, \#) = F_{\text{free}}(T_M, \#) \bigcup F_{\text{free}}(T_M, \#) \bigcup (T_M \cap T_M) \cup T_M
\]

Within this partition, each un-observable free transition leads to a block reduced to itself, and a last block gathers all the transitions which are observable or involved in a conflict. As a consequence, equivalence relation \( \# \) is weaker than the initial one \( \# \in \# \).

3. \( \Pi(T_M(T, t)) = \Pi_C((T_M(T, t)) \bigcup (F_{\text{free}}(T_M(T, t)) \setminus T_{\text{obs}}) \bigcup (\Pi(T_M(T, t)) \setminus T_{\text{obs}})
\]

**Property:** This algorithm computes a step graph which is weak-bisimilar (w.r.t \( T_{\text{obs}} \)) to the standard LTS

**Step Graph derivation** Since \( \# \) is transitive and subsumes \#', associated functions \( T_M, T_U \) and \( \Pi \) fulfils sufficient conditions expressed in table 2 for the step graph derivation (cf prop 3.4.3). Note that condition \( CB_2 \) holds as soon as \( \Pi_C((T_M(T, t)) \bigcup (F_{\text{free}}(T_M(T, t)) \bigcup (\Pi(T_M(T, t)) \setminus T_{\text{obs}} \bigcup (\Pi(T_M(T, t)) \setminus T_{\text{obs}}) \bigcup (\Pi(T_M(T, t)) \setminus T_{\text{obs}}
\]

**Weak-bisimulation w.r.t \( T_{\text{obs}} \):** \( \Pi(T_M(T, t)) \) fulfils \( CB_3 \) because of (1) the definition of partition \( \# \), (2) the definition of \( \Pi(T_M(T, t)) \) by means of the orthoprodut and (3) the introduction of step \( F_{\text{free}}(T_M(T, t)) \setminus T_{\text{obs}}

---

5 Evaluation and Conclusion

**Evaluation:** The considered example is a data base system presented in [Jen 87] as illustrative example for a state space reduction using equivalent markings.
The same example has been used in [Val 89] to illustrate state space reduction method using stubborn set.

The data base system consists in \( n \geq 2 \) managers and a mechanism ensuring mutual exclusion for critical operations. Initially, all managers are *idle* and the base is *open*. The modification of the value by a manager is modelled by transition “update and send messages” \((usm(k))\) for which a message is sent to each partners of manager \( k \). The sending manager \((k)\) waits until all other managers have received his message \((rm(k,p))\), performed an update and sent an acknowledgement \((sa(k,p))\). When all acknowledgements are available, the sending manager returns to idle and the base to open \((ra(k))\).

Fig 8.a depicts the general structure of the whole state space in the case of \( n \) managers. Cube \( k \) represents the interleaved execution of transitions \( rm(k,p) \) and \( sa(k,p) \) for \( p \in [1, N], p \neq k \). Fig 8.b depicts “cube” \( 1 \) in the case of 3 managers. For this system, pair of transitions in conflict have the following form: \((t,t)\) or \((usm(i),usm(j))\) or \((usm(i),rm(i,j))\). Consequently each “cube” will be represented in a step graph by \( 3 \) states and \( 2 \) transition Steps \( \{ rm(1,2),rm(1,3) \} \) and \( \{ sa(1,2),sa(1,3) \} \) for the cube depicted Fig 8.a). Finally the whole Step graph for a date base consisting of \( n \) managers is composed of \( 3n + 1 \) states and \( 4n \) edges.

![Fig. 8. Covering Step Graph for a \( n \) managers data base](image_url)

**Comparative Results:** The following table gives the state space size for \( n \) managers, first when no reduction is performed, then with a partial exploration by means of stubborn sets [Val 89], with a symbolic exploration by means of equivalent markings [Jen 87], finally with a partial and symbolic exploration.

<table>
<thead>
<tr>
<th></th>
<th>states</th>
<th>edges</th>
</tr>
</thead>
<tbody>
<tr>
<td>no reduction</td>
<td>( n^{3n-1} + 1 \approx n^2 \times 3^{n-2} )</td>
<td></td>
</tr>
<tr>
<td>Stubborn sets</td>
<td>( 2n^2 )</td>
<td>( 2n^2 )</td>
</tr>
<tr>
<td>Equivalent markings</td>
<td>( n^2/2 )</td>
<td>( n^2 )</td>
</tr>
<tr>
<td>Stubborn sets + Equivalent markings</td>
<td>( 2n )</td>
<td>( 2n )</td>
</tr>
<tr>
<td><strong>Covering Step Graph</strong></td>
<td>( 3n + 1 )</td>
<td>( 4n )</td>
</tr>
<tr>
<td><strong>Covering Step Graph + Equivalent markings</strong></td>
<td>4</td>
<td>4</td>
</tr>
</tbody>
</table>
All the different techniques allow the size of the state space to be reduced from exponential to polynomial. Covering Step graphs take advantage over stubborn sets and equivalent markings: the size is linear for Step graphs, while it is quadratic when stubborn sets or Equivalent markings are used. Finally, as mentioned in [Val 89], “Symbolic” methods (using symmetries) and “Partial Order” methods (using independence relation) are complementary: for this particular example, an exploration by means of Step graphs and equivalent markings furnishes the optimal result since the size of the reduced state space is constant.

Behavioural analysis using Covering Step Graphs:

The same example is used to illustrate the reduction power of the proposed approach in the framework of Weak-Bisimulation. Now the step graph derivation is conducted with respect to a set of observable events. Roughly speaking, observable events are chosen according to the set of properties to be analysed [HM 85]. In the context of the present example, two kinds of observation are considered: first, observed events are relative to critical operations ($T_{OBS_1} = \{usm(k) : \forall k \in [1, N]\} \cup \{ra(k) : \forall k \in [1, N]\}$) allowing mutual exclusion property to be verified; second, the “local service” of each manager ($T_{OBS_2}(k) = \{usm(k), RM(k), SA(k)\}$) is considered.

In the case of $T_{OBS_1}$, the obtained step graph is those obtained by general step graph derivation (Fig. 8,c). It consists in $3n + 1$ states and $4n$ edges while the minimal weak bisimilar LTS consists in $n + 1$ states and $2n$ transitions (Fig 9)

The following table summarises the obtained results for $T_{OBS_2}$. In this case, the minimal weak bisimilar LTS is constant (4 states and 5 edges) while the size of the obtained equivalent step graph seems quadratic in $k$.

<table>
<thead>
<tr>
<th>$K$</th>
<th>3</th>
<th>4</th>
<th>6</th>
<th>10</th>
</tr>
</thead>
<tbody>
<tr>
<td>no reduction</td>
<td>28/42</td>
<td>109/224</td>
<td>1459/1872</td>
<td>290.491/11.810.000</td>
</tr>
<tr>
<td>Equivalent step graph</td>
<td>10/12</td>
<td>13/16</td>
<td>19/24</td>
<td>58/94</td>
</tr>
<tr>
<td>minimal weak</td>
<td>4/5</td>
<td>4/5</td>
<td>4/5</td>
<td>4/5</td>
</tr>
</tbody>
</table>

\footnote{In order to have a better reduction with observational equivalence: events $rm(k,j)$ and $sa(k,j)$ for $k \neq j$ are respectively renamed by $RM(k)$ and $SA(k)$}
Conclusion and Future works: This paper has presented Covering Step Graphs as an alternative to LTS. CSG permits the verification of general reachability properties (deadlock detection, liveness) while Weak Bisimilar CSG (derived with respect to a set of observable events) permits the verification of specific properties expressed in Hennesy-Milner Logic [HM 85]. Two algorithms are proposed for an “on the fly generation” of such graphs. Two standard examples permit us to obtain a first evaluation of our approach: the obtained results are promising, but significant experiments remain necessary. With respect to behavioural analysis, other equivalences (maximal traces, co-simulation, ...) may be also considered and associated step graph derivation algorithms proposed. Finally, the interest of step graphs in the framework of “True concurrency” semantics may be envisaged.

References

[FM 90] J. Fernandez, L. Mounier Verifying Bisimulation on the Fly
3 rd Int. Conf. on Formal Description Techniques, Madrid, 1990

[GW 91] P. Godefroid, P. Wolper
Using Partial Orders for efficient verification of deadlock freedom and safety
properties In Computer Aided Verification, 1991, LNCS 575

[GP 93] P. Godefroid, D. Peiotin
Refining Dependencies Improves Partial-Order verification methods
In Computer Aided Verification, 1993, LNCS 697

[Esp 93] J. Espiraza
Model Checking using net unfoldings In TAPSOFT’93, 1993, LNCS 668

[HM 85] M. Hennesy, R. Milner Algebraic Laws for Nondeterminism and Concurrency
Journal of the A.C.M Volume 32 1985

In Brauer, W., Reisig, W. & Rozenberg, G. (Ed.); Petri Nets: Central Models
and their Properties. Advances in Petri Nets LNCS 254

[PF 90] D. H. Pitt, D. Freestone The derivation of conformance tests from lotos
specifications In IEEE Transactions on Software Engineering, 16(12), 1990

[McMil 95] K. L. McMillan Trace theoretic verification of asynchronous circuits using
unfoldings In Computer Aided Verification, 1995, LNCS 939

[Maz 87] A. Mazurkiewicz Trace Theory In “Petri Nets: Applications and Relationship
to other models of concurrency” LNCS 255

[Mil 85] R. Milner Communication and Concurrency Prentice Hall,

[Rei 85] W. Reisig Petri Nets: an Introduction
EATCS, Monographs on Theoretical Computer Science, Springer Verlag, 1985

[Val 89] A. Valmari Stubborn Sets for reduced state space generation
10th Int. Conf. on Application and Theory of Petri Nets, Bonn, 1989, LNCS 483

Proceedings of CONCUR’93, LNCS 715

This article was processed using the \LaTeX\ macro package with LLNCs style